

## ABSTRACT

An amplifier providing a drive signal indicative of a data input signal to a capacitive and/or resistive type load, the amplifier having a first transistor circuit adapted for converting the data input signal to a corresponding current signal in which the transistors of the first transistor circuit operate at a first voltage and having a second transistor circuit amplifying the current signal in which the transistors of the second transistor circuit operate at a second voltage. The first transistor circuit and the second transistor circuit are integrated for providing a class AB operable current output to the load.